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DFW

April 6, 2005

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Re: U.S. Patent Application No.: 10/698,805
For: *SIMPLIFIED DATA SIGNAL SUPPORT FOR DIAGRAMMING
ENVIRONMENT LANGUAGES*
Inventors: Szpak, Peter, *et al.*
Filed: October 31, 2003
Our Ref. No.: MWS-058

Dear Sir:

I enclose herewith for filing in the above-identified application the following:

1. Information Disclosure Statement;
2. PTO Form SB/08;
3. Copies of selected references cited in PTO Form SB/08 (7);
4. A copy of the International Search Report; and
5. A Return Postcard.

No additional costs are believed to be due in connection with the filing of this Information Disclosure Statement. However, please charge any necessary fees in connection with the enclosed statement to our Deposit Order Account No. 12-0080. For this purpose, a duplicate of this sheet is attached.

I hereby certify that this correspondence is deposited with the United States Postal Service as first class mail in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on:

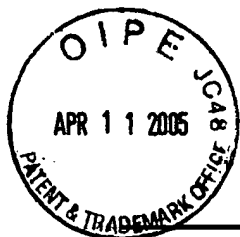
April 6, 2005

Date

Kevin J. Canning, Esq. Registration No. 35,470

Respectfully submitted,
LAHIVE & COCKFIELD, LLP

Kevin J. Canning, Esq.
Registration No. 35,470
Attorney for Applicants



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: Szpak, Peter, *et al.*

Serial No.: 10/698,805

Filed: October 31, 2003

For: *SIMPLIFIED DATA SIGNAL SUPPORT FOR
DIAGRAMMING ENVIRONMENT LANGUAGES*

Attorney Docket No.: MWS-058

Group Art Unit: 2123

Examiner: Not Yet Assigned

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Certificate of First Class Mailing (37 CFR §1.8(a))

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April 6, 2005

Date of Signature and of Mail Deposit

By:

Kevin J. Canning, Esq.
Registration No. 35,470
Attorney for Applicants

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

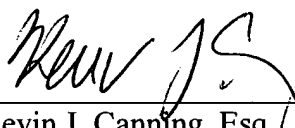
Applicants and their Attorney are aware of the following publications and information, listed on the attached PTO Form SB/08, and in accordance with 37 CFR §1.97 hereby submit these publications for the Examiner's consideration. These references were cited in an International Search Report dated February 28, 2005 during the prosecution of PCT/US2004/034904 which corresponds to the above referenced application. A copy of the report and each cited publication is enclosed. A copy of the cited U.S. patent (Reference ID No. A1) is not enclosed with this statement. Copies of non-patent literature (Reference ID Nos. A2-A8) are enclosed in accordance with 37 CFR 1.98.

This statement is not to be interpreted as a representation that the cited publications are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such publication is prior art. Moreover, Applicants understand that the Examiner will make an independent evaluation of the cited publications.

In accordance with 37 CFR §1.97(e)(1), the undersigned hereby certifies that each item contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application *for the first time* not more than three months prior to the filing of this statement.

In accordance with 37 CFR §1.97(b)(1), no additional costs are believed to be due in connection with the filing of this Information Disclosure Statement. However, please charge any necessary fees in connection with the enclosed statement to our Deposit Order Account No. 12-0080.

Respectfully submitted,
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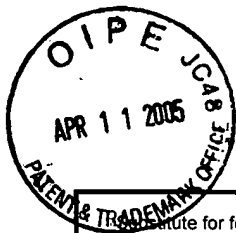


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Enclosures



PTO/SB/08a/b (08-03)

Approved for use through 07/31/2006. OMB 0651-0031
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/698,805
				Filing Date	October 31, 2003
				First Named Inventor	Szpak, Peter
				Art Unit	2123
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	MWS-058

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	A1	5,371,851	12-06-1994	Pieper, <i>et al.</i>	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
	A2	Drongowski, <i>et al.</i> "A Graphical Hardware Design Language." <i>Proceedings of the Design Automation Conference</i> , June 12, 1988, paper 9.2, pp. 108-114.		
	A3	Lavagno, <i>et al.</i> "A Simulink ©-based Approach to System Level Design and Architecture Selection." <i>Proceedings of the Euromicro Conference</i> , September 5, 2000, pp. 76-83.		
	A4	May, P. "A Flexible VHDL Test Bench Architecture." <i>Military Communications Conference, IEEE San Diego, CA</i> , October 11, 1992, pp. 979-985.		
	A5	Molson, P. "Accelerating Intellectual Property Design Flow Using Simulink® for System on a Programmable Chip." <i>Conference Record of the 35th Asilomar Conference on Signals, Systems & Computers</i> , November 4, 2001, pp. 454-457.		
	A6	Mrad, <i>et al.</i> "Real-Time Control of Free-Standing Cart-Mounted Inverted Pendulum Using LabVIEW RT." <i>Proceedings of World Congress on Industrial Applications of Electrical Energy and 35th IEEE-IAS Annual Meeting</i> , October 8, 2000, pp. 1291-1298.		
	A7	Skahill, K. "Entities and Architectures." <i>VHDL for Programmable Logic</i> . Cypress Semiconductor, Addison Wesley, Menlo Park, CA, 1996, pp. 105-158.		
	A8	Skelly, <i>et al.</i> "A LabVIEW Approach to Instrumentation for the TFTR Bumper Limiter Alignment Project." <i>Proceedings of the IEEE/NPSS Symposium on Fusion Engineering</i> , September 30, 1991, pp. 765-768.		

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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